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Dr. Junzheng Cao was born in Shaanxi, China in 1960. He received his B.Eng. and M.Sc. degrees from Xi'an Jiaotong University, China, in 1982 and 1985, respectively, and the Ph.D. degree from South Bank University, UK, in 1997. He is currently a professorate senior engineer and deputy chief engineer at an electric research institute of SGCC. He was entitled to China's National Distinguished Expert of "1000-Elit Program" in 2010, and a guest professor of Xian Jiaotong University since 2012. Prior to returning China in 2010, he served as a principal engineer and an HVDC expert at AREVA/ALSTOM T&D.

Dr. Cao is a member of IEEE, IET, Cigre, and several IEC and Cigre working groups. His research interests are in the development and engineering of power electronics for FACTS, HVDC and future DC grid applications. During the last five years, he has authored or co-authored more than 39 publications, was a co-inventor on 45 Chinese and foreign patents, and jointly won 3 Chinese ministerial science and technology awards.